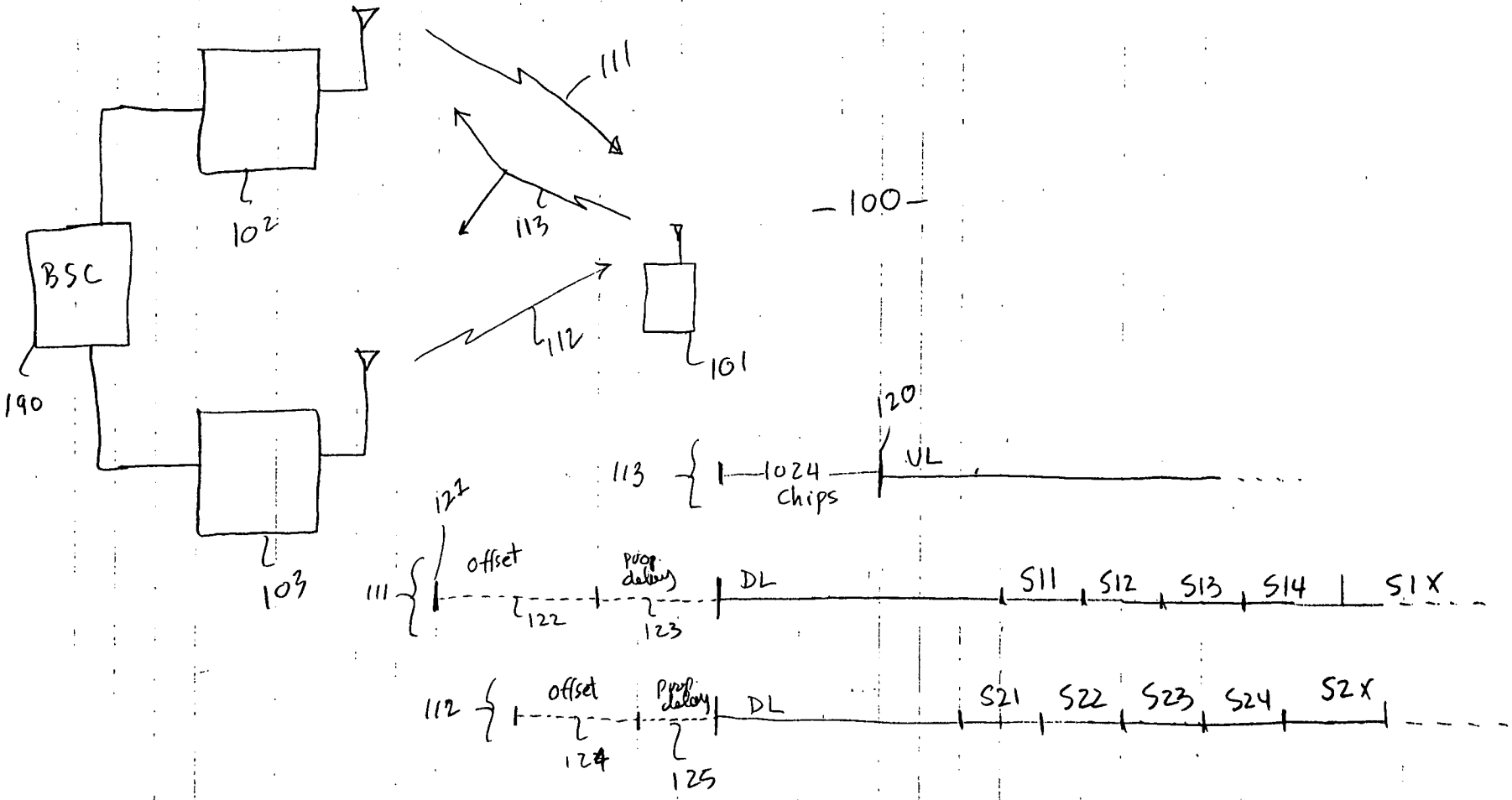
BSC
190
102
103
111
113
112
101
100
120
VL
1024 chips
113
127
offset
prop. delay
DL
111
122
123
S11
S12
S13
S14
S1X
112
offset
prop. delay
DL
124
125
S21
S22
S23
S24
S2X

FIG. 1

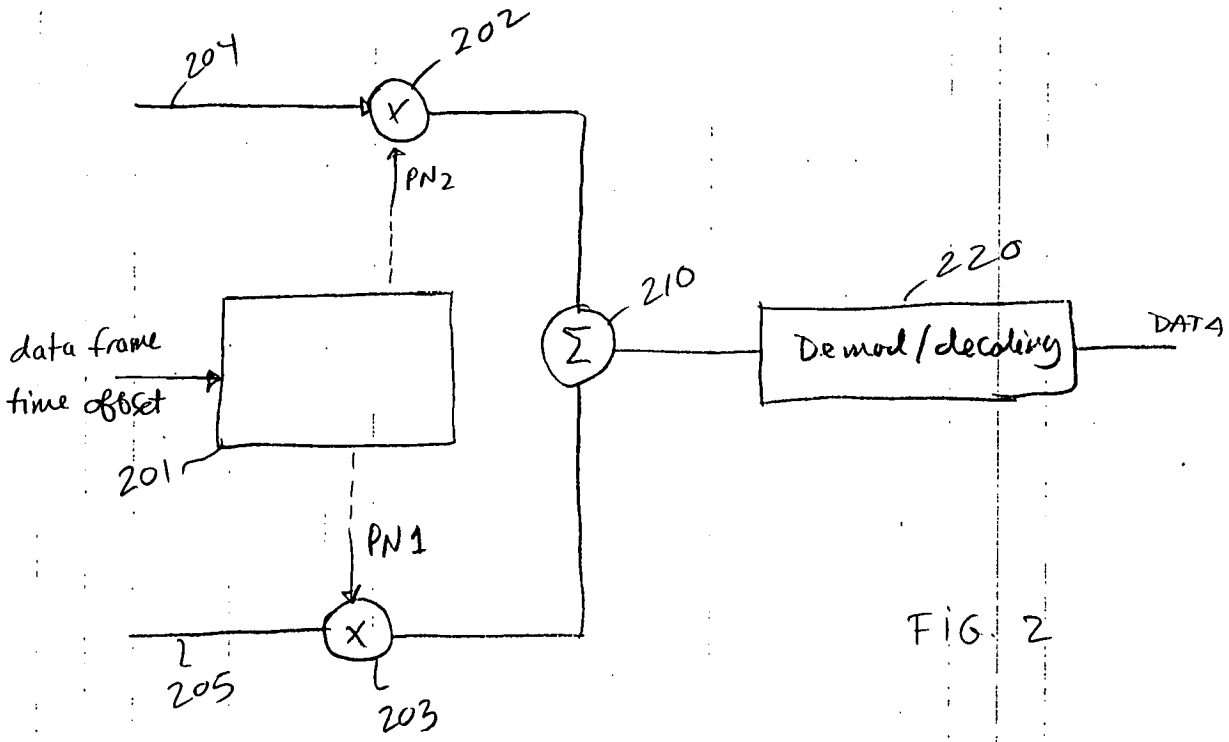
204
202
X
PN2
data frame
time offset
201
210
Σ
220
Demod/decoding
DATA
PN1
X
205
203

FIG. 2